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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/711,509	09/23/2004	Fang-Chen Luo	12405-US-PA-0P	5508	
31561	7590 03/31/2005		EXAMINER		
JIANQ CH 7 FLOOR-1	YUN INTELLECTUAL	BREWSTER, WILLIAM M			
	T ROAD, SECTION 2		ART UNIT	PAPER NUMBER	
TAIPEI, 1	100		2823		
TAIWAN					

Please find below and/or attached an Office communication concerning this application or proceeding.

			<b></b>					
		Applicati	on No.	Applicant(s)				
Office Action Summary		10/711,5	09	LUO ET AL.				
		Examine	<u> </u>	Art Unit	<del></del>			
		William M	. Brewster	2823				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
THE MAII - Extensions after SIX (6 - If the perio - If NO perio - Failure to r Any reply r	ENED STATUTORY PERIOD FOLING DATE OF THIS COMMUNIC of time may be available under the provisions of MONTHS from the mailing date of this communication of the specified above is less than thirty (30 d for reply is specified above, the maximum state of the specified above, the specified above of the specified above, the specified above of the specified above of the specified above of the specified above.	CATION.  of 37 CFR 1.136(a). In no even inication.  of ays, a reply within the state of the apply and will, by statute, cause the apply.	ent, however, may a reply be sutory minimum of thirty (30) double expire SIX (6) MONTHS froblication to become ABANDON	timely filed  ays will be considered timely.  m the mailing date of this commun  IED (35 U.S.C. § 133).	nication.			
Status								
1)⊠ Res	sponsive to communication(s) filed	d on <u>31 January 200</u>	<u>95</u> .					
	This action is <b>FINAL</b> . 2b) This action is non-final.							
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of	of Claims							
4a) 5)□ Cla 6)⊠ Cla 7)□ Cla	im(s) <u>1-10</u> is/are pending in the apost the above claim(s) is/are im(s) is/are allowed. im(s) <u>1-10</u> is/are rejected. im(s) is/are objected to. im(s) are subject to restrict	e withdrawn from co						
Application I	Papers							
10)⊠ The App Rep	specification is objected to by the drawing(s) filed on 23 September licant may not request that any objectant drawing sheet(s) including oath or declaration is objected to	r 2003 is/are: a) ☐ a tion to the drawing(s) the correction is requir	pe held in abeyance. S red if the drawing(s) is o	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.	121(d).			
Priority unde	r 35 U.S.C. § 119							
12) Acki a) A 1. 2. 3.	nowledgment is made of a claim for b) Some * c) None of:  Certified copies of the priority of Certified copies of the priority	locuments have bee locuments have bee of the priority documental al Bureau (PCT Rul	en received. en received in Applica ents have been receive e 17.2(a)).	ition No ved in this National Stag	j <b>e</b>			
Attachment(s)								
	References Cited (PTO-892)	· · · · · · · · · · · · · · · · · · ·	4) Interview Summar					
3) 🔲 Information	Praftsperson's Patent Drawing Review (PT Disclosure Statement(s) (PTO-1449 or Fs)/Mail Date	·	Paper No(s)/Mail (5) Notice of Informal 6) Other:	Patent Application (PTO-152)	)			

Art Unit: 2823

#### **DETAILED ACTION**

#### **Drawings**

Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the examiner does not accept the changes, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 2, 8-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Lee et al., US Patent No. 6,737,305 B2.

Art Unit: 2823

Lee anticipates a manufacturing method of a thin film transistor (TFT), comprising:

col. 4, line 54 - col. 5, line 61, in fig. 4A, forming a gate 102 over a substrate 100; in fig. 4B, forming an inter-gate dielectric layer 104 over the substrate covering the gate; in fig. 4C, forming a channel layer 106 over a portion of the inter-gate dielectric layer at least over the gate, wherein the channel layer comprises a lightly doped amorphous silicon layer, col. 5, lines 4-18; and

in fig. 4E, forming source/drain regions 108 over the channel layer, wherein the source/drain regions are separated by a distance;

limitations from claim 2, the manufacturing method of claim 1, wherein the channel layer comprises an N-type lightly doped amorphous silicon layer, col. 5, lines 4-18;

limitations from claim 8, the manufacturing method of claim 1, wherein the step of forming the channel layer comprises:

in fig. 4C, forming a first lightly doped sub-amorphous silicon layer 106A over the portion of the inter-gate dielectric layer at a first deposition rate (LDR, low deposition rate); and forming a second lightly doped sub-amorphous silicon layer 106B over the first lightly doped sub-amorphous silicon layer at a second deposition rate, (HDR, high deposition rate), wherein the first deposition rate is lower than the second deposition rate, col. 5, lines 4-18;

limitations from claim 9, the manufacturing method of claim 1, further comprising a step of forming, in fig. 4E an ohmic contact layer (not shown, but described)

Art Unit: 2823

over the channel layer between the step of forming the channel layer and the step of forming the source/drain regions 108, col. 5, lines 25-32; limitations from claim 10, the manufacturing method of claim 1, further comprising a step of forming, in fig. 4F, a protection layer 110 over the substrate after the step of forming the source/drain regions covering the source/drain regions, the channel layer and the inter-gate dielectric layer, col. 5, lines 33-38. limitations from claims 6 and 7, the manufacturing method of claim 1; wherein the step of forming the channel layer comprises performing a chemical vapor deposition (CVD) process using a reaction gas mixture comprising a silane (SiH4), hydrogen (H2), col. 5, lines 55-61.

### Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 3-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee as applied to claims 1, 2, 8-10 above, and further in view of Yang et al., US Publication No. 2002/0102781 A1.

Lee does not specify using phosphine or boroethane, but Yang does. Yang teaches doping the amorphous silicon channel layer of the TFT with phosphine for an n-

Art Unit: 2823

type TFT or boroethane for a p-type TFT, p. 2, ¶ 25. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to recognize that combining Yang's process with Lee's invention would have been beneficial because the well-known industry dopants are readily available and cost effective.

Neither Lee nor Yang specify for claims 4 and 5, the concentration of the dopants, or claims 6 and 7, the ratio of the reactants. However, the practitioner may optimize these ranges:

"Normally, it is to be expected that a change in temperature, or in concentration, or in both, would be an unpatentable modification. Under some circumstances, however, changes such as these may impart patentability to a process if the particular ranges claimed produce a new and unexpected result which is different in kind and not merely degree from the results of the prior art . . . such ranges are termed 'critical ranges' and the applicant has the burden of proving such criticality . . . More particularly, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation."

In re Aller 105 USPQ 233, 255 (CCPA 1955). See also In re Waite 77 USPQ 586 (CCPA 1948); In re Scherl 70 USPQ 204 (CCPA 1946); In re Irmscher 66 USPQ 314 (CCPA 1945); In re Norman 66 USPQ 308 (CCPA 1945); In re Swenson 56 USPQ 372 (CCPA 1942); In re Sola 25 USPQ 433 (CCPA 1935); In re Dreyfus 24 USPQ 52 (CCPA 1934).

Note that the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising there from. Where patentability is aid to be based upon particular chosen dimensions or upon another

Art Unit: 2823

variable recited in a claim, the Applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 571-272-1854. The examiner can normally be reached on Full Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

29 March 2005

William M. Brewster

**WB**